



# GSX30N60MF

## MOSFET

Metal Oxide Semiconductor Field Effect Transistor

Super Junction MOSFET

600V Super Junction Power Transistor

GSX30N60MF

Data Sheet

Ver 0

2018-5-10

# 600V 30A Power MOSFET

## ■ Description

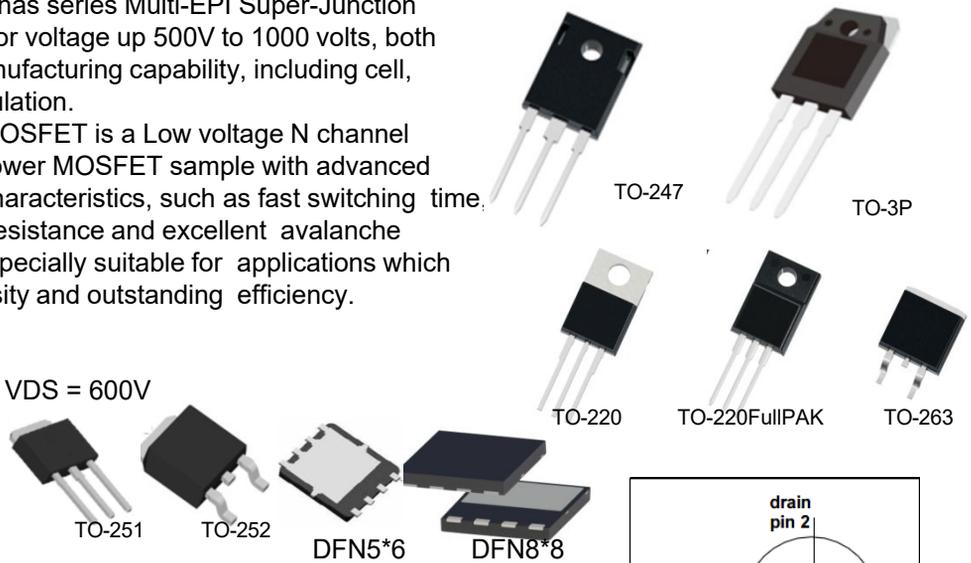
Group Semiconductor(GS) has series Multi-EPI Super-Junction power MOSFET platforms for voltage up 500V to 1000 volts, both with design service and manufacturing capability, including cell, termination design and simulation.

The GS 600V 30A power MOSFET is a Low voltage N channel Multi-EPI Super-Junction power MOSFET sample with advanced technology to have better characteristics, such as fast switching time, low Ciss and Crss, low on resistance and excellent avalanche characteristics, making it especially suitable for applications which require superior power density and outstanding efficiency.

## ■ Features

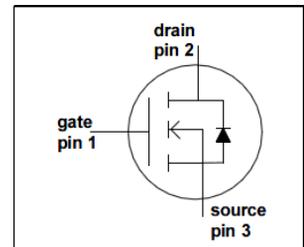
RDS(ON)=0.115Ω @VGS = 10 VDS = 600V

ID (@ VGS=10V) = 15A



## ■ PKG

GSA30N60MF	GSP30N60MF	GSB30N60MF	GSD30N60MF	GSS30N60MF
TO-220Fullpak	TO-220	TO-263	TO-252	TO-251
GSW30N60MF	GSJ30N60MF	GSN30N60MF	GSM30N60MF	
TO-247	TO-3P	DFN5*6	DFN8*8	



## ■ Absolute Maximum Ratings (TC = 25° C, unless otherwise specified)

Symbol	Parameter	GSP30N60MF	GSA30N60MF	Unit
V <sub>DSS</sub>	Drain-Source Voltage	600		V
I <sub>D</sub>	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C)	30* 12*		A
I <sub>DM</sub>	Drain Current - Pulsed (Note 1)	60		A
V <sub>GSS</sub>	Gate-Source voltage	±30		V
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	520		mJ
I <sub>AR</sub>	Avalanche Current (Note 1)	3.5		A
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)	1		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15		V/ns
dVds/dt	Drain Source voltage slope (Vds=480V)	50		V/ns
P <sub>D</sub>	Power Dissipation (TC = 25°C)	151	35	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150		°C
T <sub>L</sub>	Max. Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		°C

# 600V 30A Power Transistor

## ■ Electrical Characteristics (T<sub>J</sub>=25° C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA, T <sub>J</sub> = 25°C	600	--	--	V
		V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA, T <sub>J</sub> = 150°C	--	650	--	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C	--	0.6	--	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V -T <sub>J</sub> =25 °C -T <sub>J</sub> = 150°C	--	10	6 -	μA μA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30V, V <sub>DS</sub> = 0V	--	--	100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30V, V <sub>DS</sub> = 0V	--	--	-100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	--	4	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A	--	0.1	0.115	Ω
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 40V, I <sub>D</sub> = 15A	--	16	--	S
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	--	2510	-	pF
C <sub>oss</sub>	Output Capacitance		--	105	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	9	--	pF
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 400V (Note 4)	--	25	--	ns
t <sub>r</sub>	Turn-On Rise Time		--	17	--	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		--	130	--	ns
t <sub>f</sub>	Turn-Off Fall Time		--	11	--	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 520V, I <sub>D</sub> = 10A V <sub>GS</sub> = 10V (Note 4)	--	90	120	nC
Q <sub>gs</sub>	Gate-Source Charge		--	8.5	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	13	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current		--	--	20	A
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		--	--	60	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A	--	0.9	1.5	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A dI <sub>F</sub> /dt = 100A/μs	--	189	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	5.8	--	μC

### NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L=60mH, I<sub>AS</sub>=3A, V<sub>DD</sub>=150V, Starting T<sub>J</sub>=25 °C
3. I<sub>SD</sub>≤4.5A, di/dt ≤ 200A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25 °C
4. Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 2%
5. Essentially Independent of Operating Temperature Typical Characteristics

# 600V 30A Power Transistor

## ■ Thermal Characteristics

Symbol	Parameter	GSA30N60MF	GSP30N60M F	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.2	1.2	$^{\circ}\text{C}/\text{W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	0.5	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62	62	$^{\circ}\text{C}/\text{W}$

Symbol	Parameter	Value (TO220)	Unit
$R_{\theta JA}^{(6)}$	Maximum Junction-to-Ambient	82	$^{\circ}\text{C}/\text{W}$
$R_{\theta CS}^{(6)}$	Maximum Case-to-sink	0.6	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}^{(7),(8)}$	Maximum Junction-to-Case $\theta$	4.1	$^{\circ}\text{C}/\text{W}$

1. The power dissipation PD is based on  $T_J(\text{MAX})=150^{\circ}\text{C}$  in a TO251 package, using junction-to-case thermal resistance.

2. Repetitive rating, pulse width limited by junction temperature  $T_J(\text{MAX})=150^{\circ}\text{C}$ .

3.  $L=1\text{mH}$ , Starting  $T_J=25^{\circ}\text{C}$ .

4.  $L=10\text{mH}$ , starting  $T_J=25^{\circ}\text{C}$ .

5.  $L=60\text{mH}$ , starting  $T_J=25^{\circ}\text{C}$ .

6. The tests are performed with the device with  $T_A=25^{\circ}\text{C}$ .

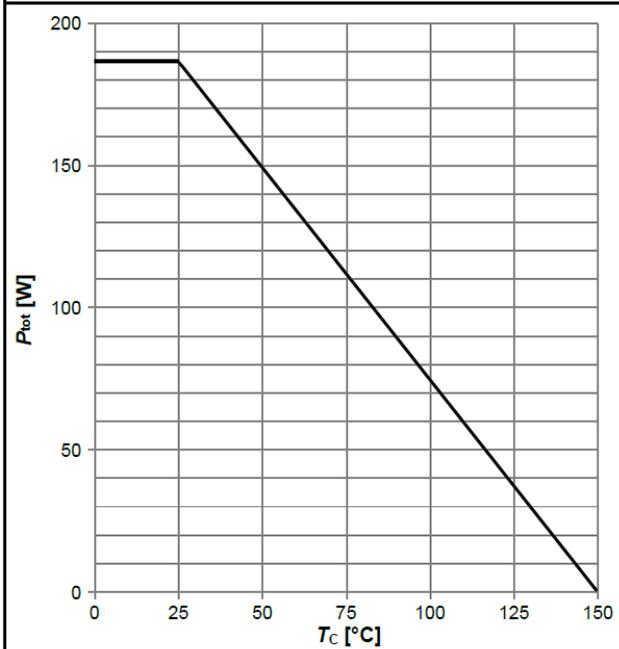
7. The  $R_{\theta JA}$  is the sum of the thermal impedance from junction to case  $R_{\theta JC}$  and case to ambient.

8. These curves are based on the junction-to-case thermal impedance, assuming a maximum junction temperature of  $T_J(\text{MAX})=150^{\circ}\text{C}$ .

# 600V 30A Power Transistor

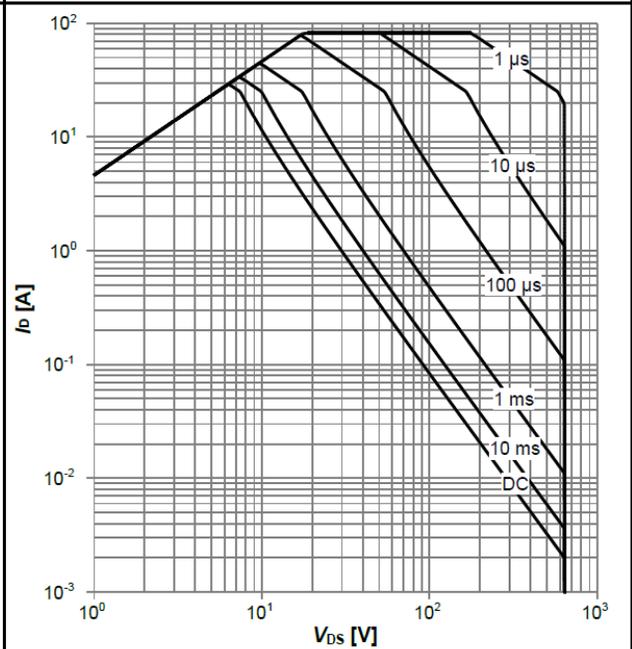
## Typical Performance Characteristics

Diagram 1: Power dissipation



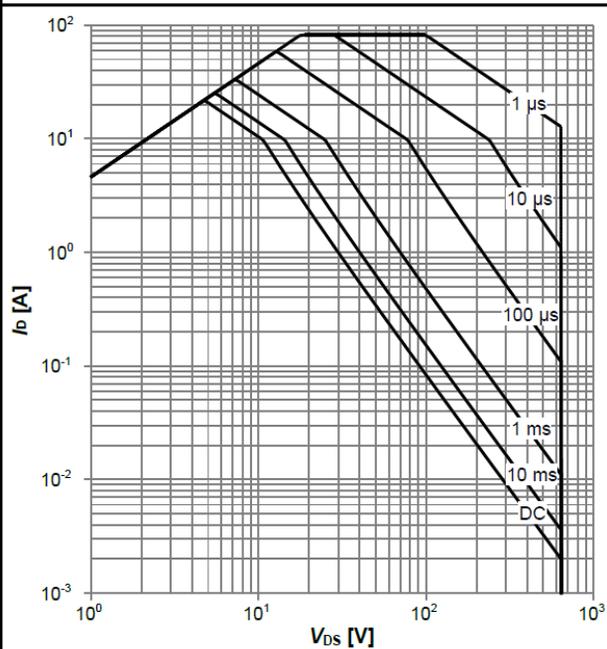
$$P_{tot}=f(T_C)$$

Diagram 2: Safe operating area



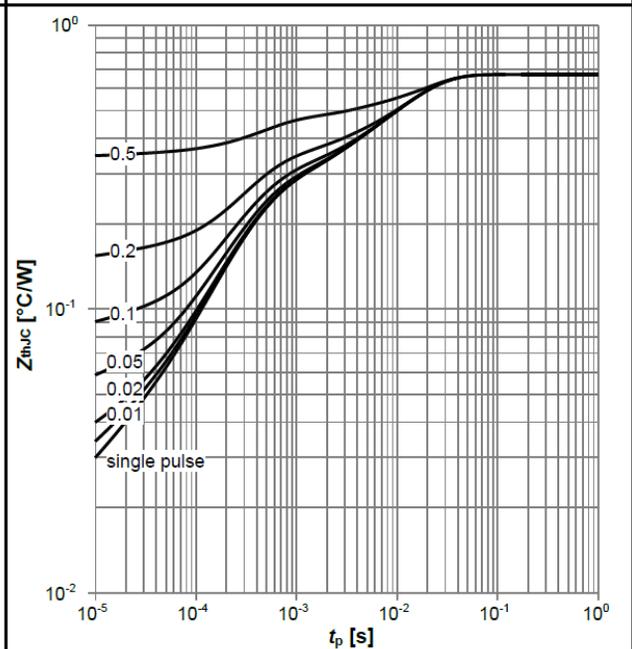
$$I_D=f(V_{DS}); T_C=25\text{ }^\circ\text{C}; D=0; \text{parameter: } t_p$$

Diagram 3: Safe operating area



$$I_D=f(V_{DS}); T_C=80\text{ }^\circ\text{C}; D=0; \text{parameter: } t_p$$

Diagram 4: Max. transient thermal impedance

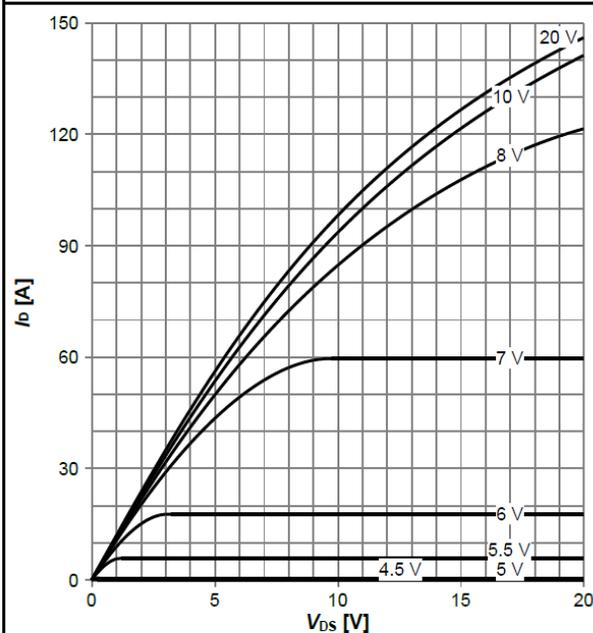


$$Z_{thJC}=f(t_p); \text{parameter: } D=t_p/T$$

# 600V 30A Power Transistor

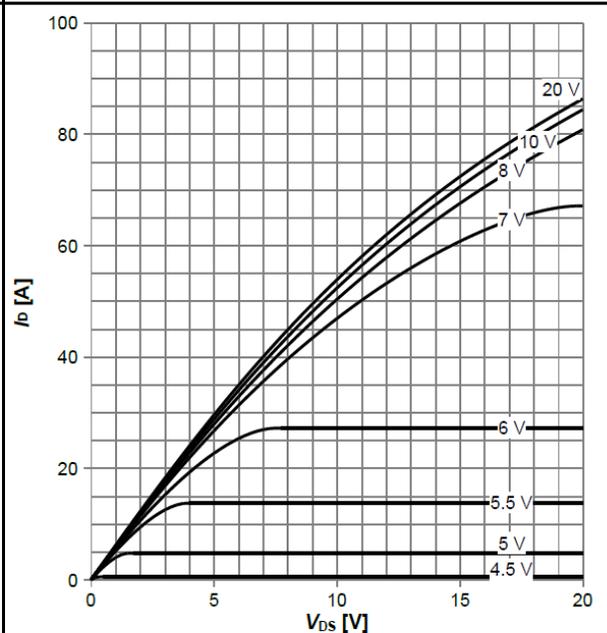
## Typical Performance Characteristics

Diagram 5: Typ. output characteristics



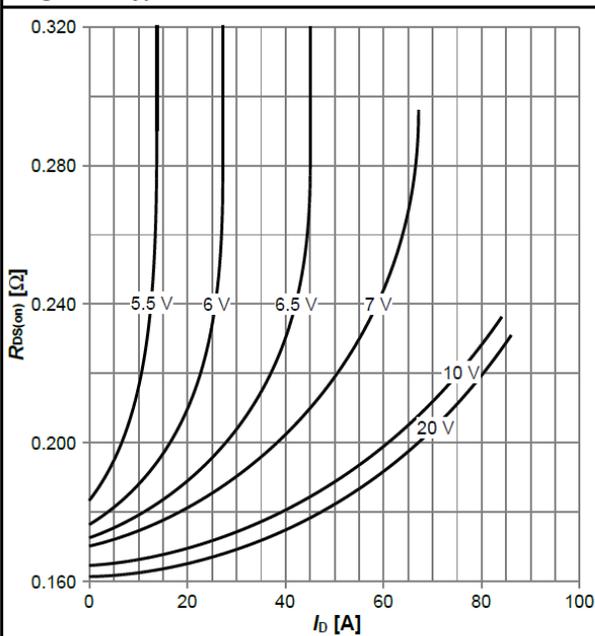
$I_D = f(V_{DS})$ ;  $T_J = 25\text{ °C}$ ; parameter:  $V_{GS}$

Diagram 6: Typ. output characteristics



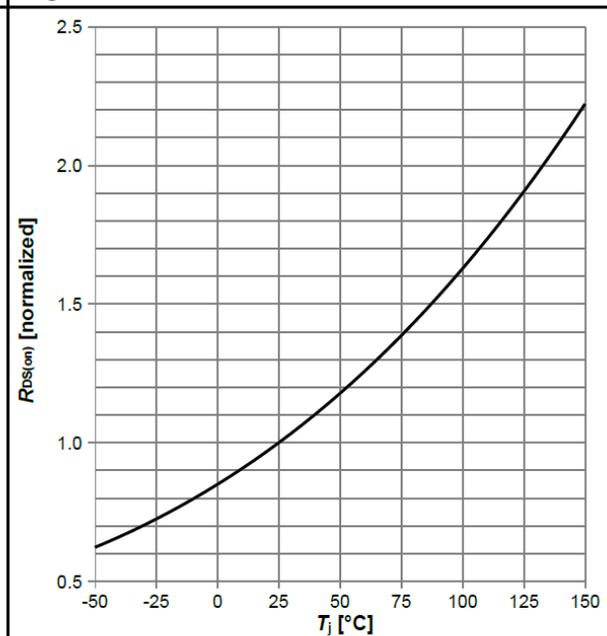
$I_D = f(V_{DS})$ ;  $T_J = 125\text{ °C}$ ; parameter:  $V_{GS}$

Diagram 7: Typ. drain-source on-state resistance



$R_{DS(on)} = f(I_D)$ ;  $T_J = 125\text{ °C}$ ; parameter:  $V_{GS}$

Diagram 8: Drain-source on-state resistance

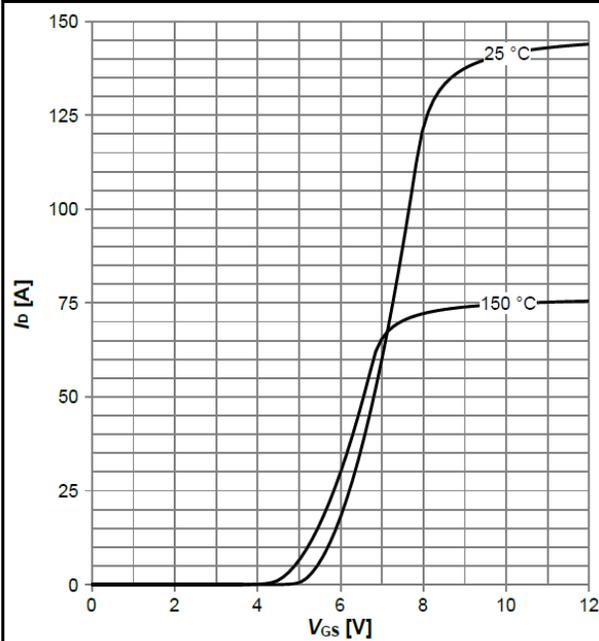


$R_{DS(on)} = f(T_J)$ ;  $I_D = 9.7\text{ A}$ ;  $V_{GS} = 10\text{ V}$

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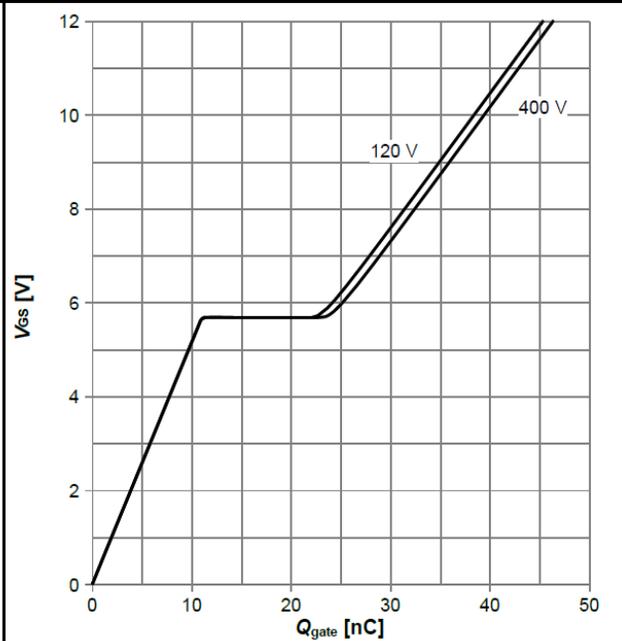
## Typical Performance Characteristics

Diagram 9: Typ. transfer characteristics



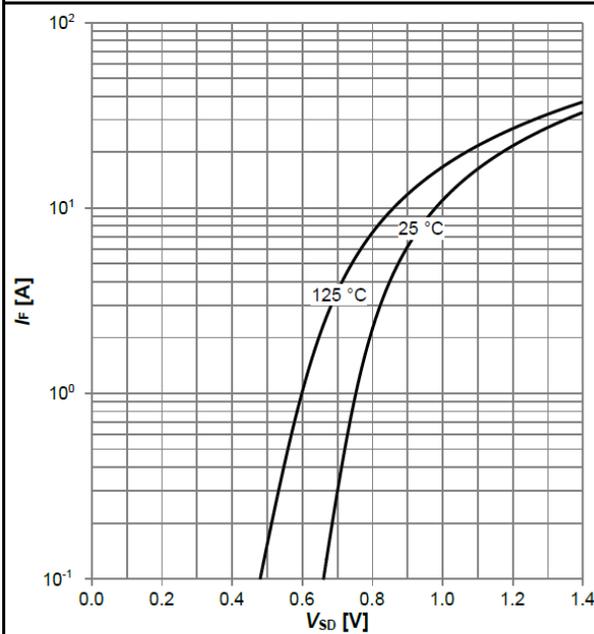
$I_D = f(V_{GS})$ ;  $V_{DS} = 20V$ ; parameter:  $T_j$

Diagram 10: Typ. gate charge



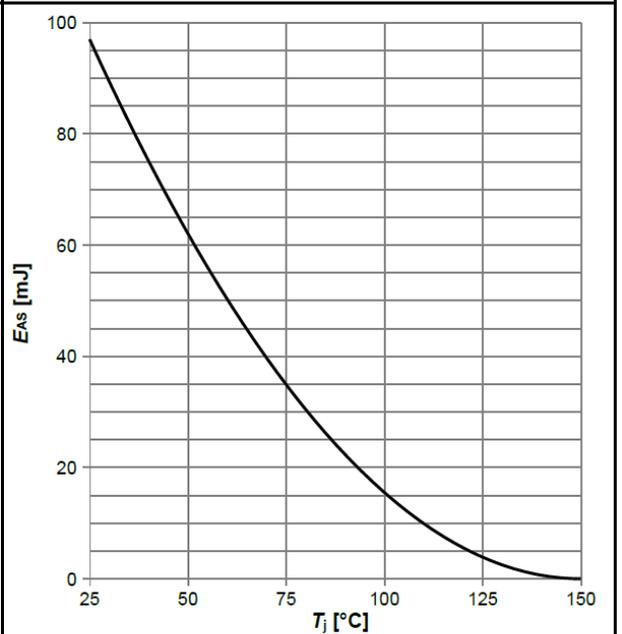
$V_{GS} = f(Q_{gate})$ ;  $I_D = 9.7 A$  pulsed; parameter:  $V_{DD}$

Diagram 11: Forward characteristics of reverse diode



$I_F = f(V_{SD})$ ; parameter:  $T_j$

Diagram 12: Avalanche energy

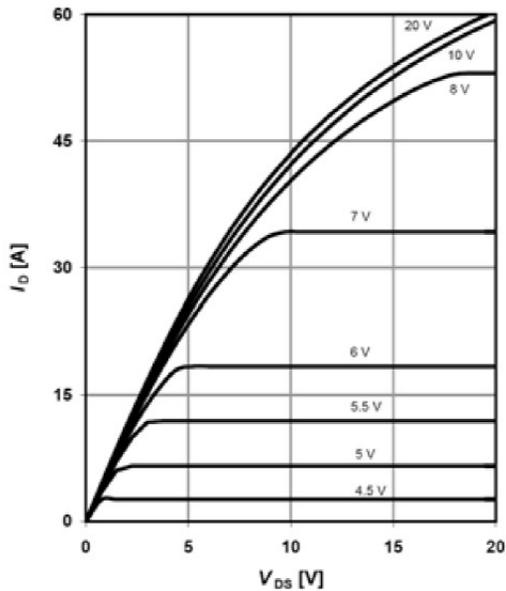


$E_{As} = f(T_j)$ ;  $I_D = 4.7 A$ ;  $V_{DD} = 50 V$

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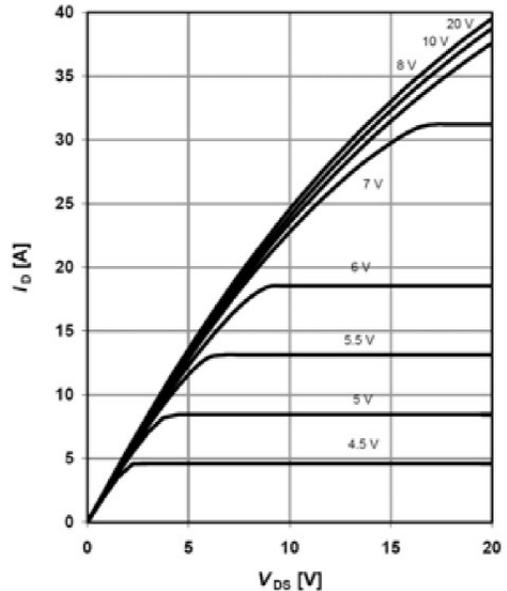
## Typical Performance Characteristics

Typ. output characteristics  $T_c=25\text{ }^\circ\text{C}$



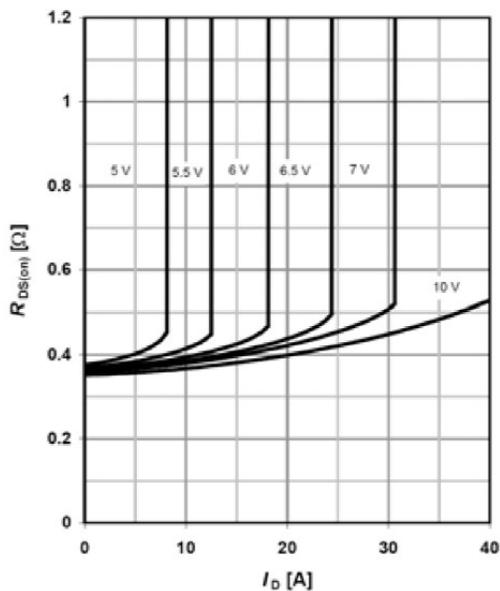
$I_D=f(V_{DS}); T_j=25\text{ }^\circ\text{C};$  parameter:  $V_{GS}$

Typ. output characteristics  $T_j=125\text{ }^\circ\text{C}$



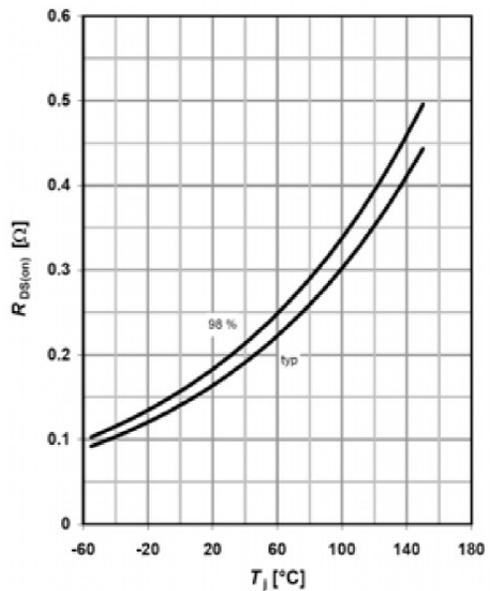
$I_D=f(V_{DS}); T_j=125\text{ }^\circ\text{C};$  parameter:  $V_{GS}$

Typ. drain-source on-state resistance



$R_{DS(on)}=f(I_D); T_j=125\text{ }^\circ\text{C};$  parameter:  $V_{GS}$

Drain-source on-state resistance

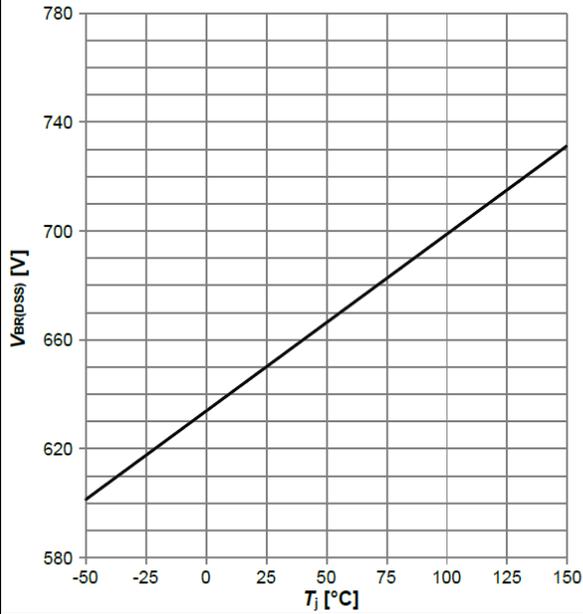


$R_{DS(on)}=f(T_j); I_D=9.5\text{ A}; V_{GS}=10\text{ V}$

# 600V 30A Power Transistor

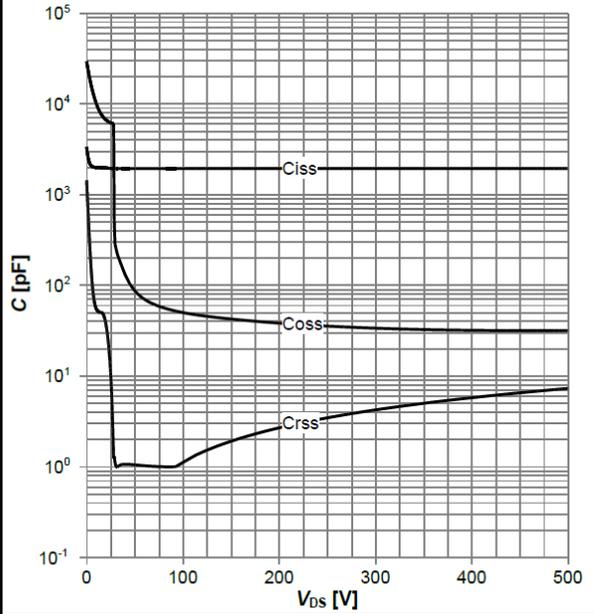
## Typical Performance Characteristics

Diagram 13: Drain-source breakdown voltage



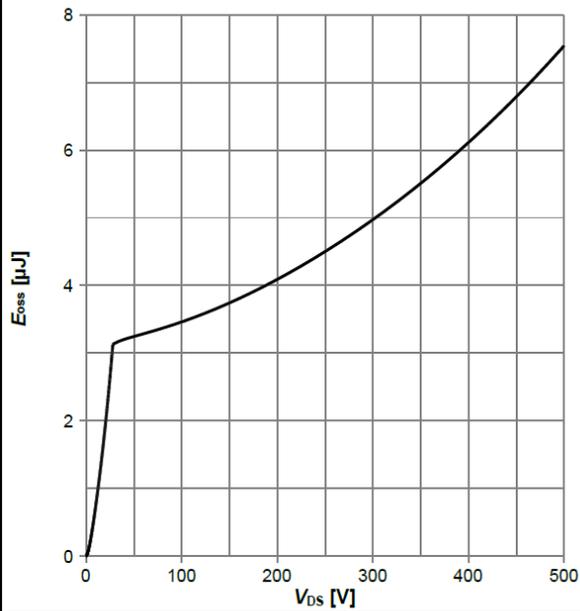
$V_{BR(DSS)}=f(T_j); I_D=1\text{ mA}$

Diagram 14: Typ. capacitances



$C=f(V_{DS}); V_{GS}=0\text{ V}; f=250\text{ kHz}$

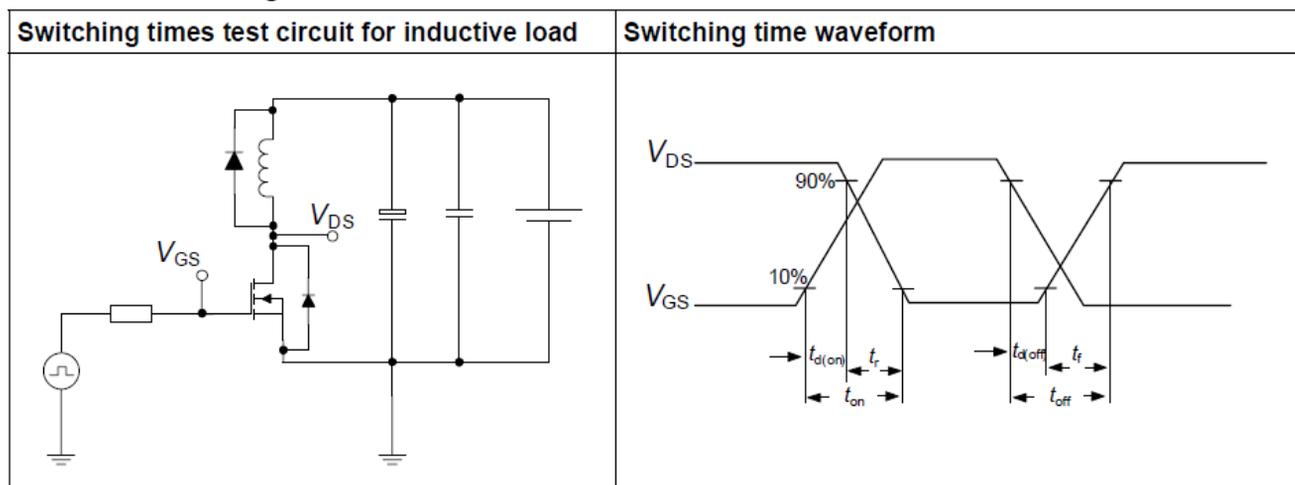
Diagram 15: Typ. Coss stored energy



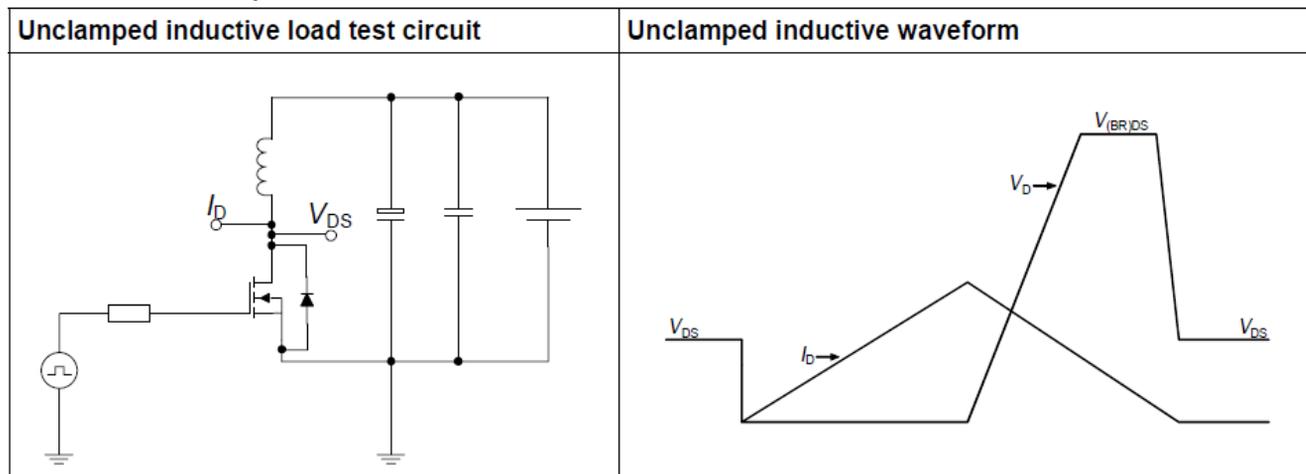
$E_{oss}=f(V_{DS})$

# 600V 30A Power Transistor

**Table 20** Switching times test circuit and waveform for inductive load



**Table 21** Unclamped inductive load test circuit and waveform



**Table 22** Test circuit and waveform for diode characteristics

