



# GSX150N15TH

## MOSFET

Metal Oxide Semiconductor Field Effect Transistor

Shield Gate Trench MOSFET

150V Power Transistor

GSX150N15TH

Data Sheet

Ver 0

2022-2-28

# 150V 186A Power MOSFET

## ■ Description

Group Semiconductor(GS) has series Shield Gate Trench power MOSFET platforms for voltage up 40V to 300 volts, both with design service and manufacturing capability, including cell, termination design and simulation.

The GS 150V 186A power MOSFET is a Low voltage N channel Shield Gate Trench power MOSFET sample with advanced technology to have better characteristics, such as fast switching time, low Ciss and Crss, low on resistance and excellent avalanche characteristics, making it especially suitable for applications which require superior power density and outstanding efficiency.

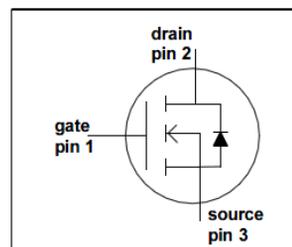
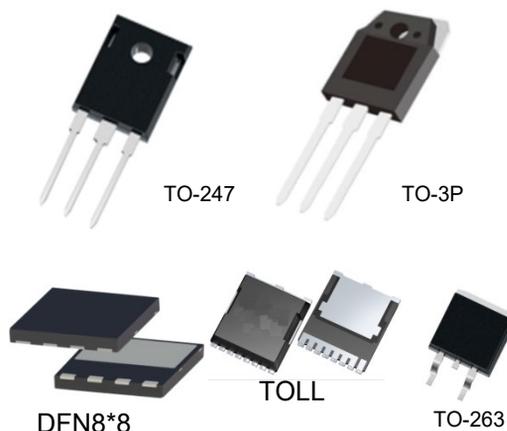
## ■ Features

RDS(ON)=4mΩ @VGS = 10V

VDS = 150V

ID (@ VGS=10V) = 186A

## ■ PKG



GSW150N15TH	GSJ150N15TH	GSL150N15TH	GSM150N15TH	GSB150N15TH
TO-247	TO-3P	Toll	DFN8*8	TO-263

## ■ Absolute Maximum Ratings (TC = 25° C, unless otherwise specified)

Symbol	Parameter	GSW150N15TH	GSJ150N15TH	Unit
V <sub>DSS</sub>	Drain-Source Voltage	150		V
I <sub>D</sub>	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C)	186* 156*		A
I <sub>DM</sub>	Drain Current - Pulsed (Note 1)	507		A
V <sub>GSS</sub>	Gate-Source voltage	±20		V
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	420		mJ
P <sub>D</sub>	Power Dissipation (TC = 25°C)	341	341	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150		°C
T <sub>L</sub>	Max. Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		°C

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## ■ Electrical Characteristics (T<sub>J</sub>=25° C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA, T <sub>J</sub> = 25°C	150	--	--	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C	--	60	--	mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 120V, V <sub>GS</sub> = 0V -T <sub>J</sub> =25 °C -T <sub>J</sub> = 150°C	--	--	1 100	μA μA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 20V, V <sub>DS</sub> = 0V	--	--	100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -20V, V <sub>DS</sub> = 0V	--	--	-100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	3	--	5	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 100A	--	3.6	4.5	mΩ
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub>  >2 I <sub>D</sub>  *R <sub>DS(on)</sub> max, I <sub>D</sub> =100 A	--	150	--	S
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 75V, V <sub>GS</sub> = 0V, f = 1.0MHz	--	6000	-	pF
C <sub>oss</sub>	Output Capacitance		--	1500	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	34	--	pF
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =75 V, V <sub>GS</sub> =10 V, I <sub>D</sub> =100 A, R <sub>G,ext</sub> =2.7 Ω	--	23	--	ns
t <sub>r</sub>	Turn-On Rise Time		--	90	--	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		--	29	--	ns
t <sub>f</sub>	Turn-Off Fall Time		--	62	--	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DD</sub> =75 V, I <sub>D</sub> =100 A, V <sub>GS</sub> =0 to 10 V	--	80	100	nC
Q <sub>gs</sub>	Gate-Source Charge		--	35	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	16	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current		--	--	186	A
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		--	--	507	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 12A	--	0.9	1.5	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>R</sub> =128 V, I <sub>F</sub> =100 A, diF/dt=100 A/μs	--	57	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	76	--	μC

### NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L=0.08mH, I<sub>AS</sub>=100A, V<sub>DD</sub>=150V, Starting T<sub>J</sub>=25 °C
3. Pulse Test: Pulse width ≤ 300us, Duty Cycle ≤ 2%
4. Essentially Independent of Operating Temperature Typical Characteristics

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## ■ Thermal Characteristics

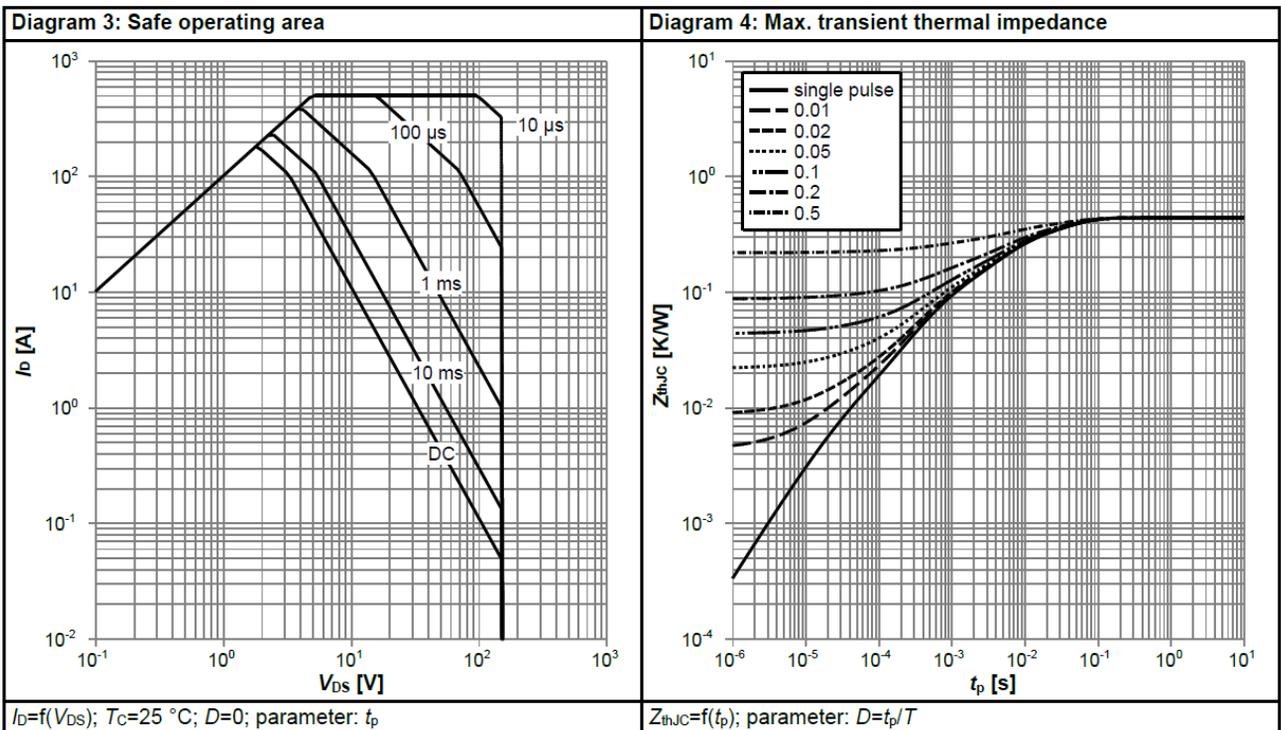
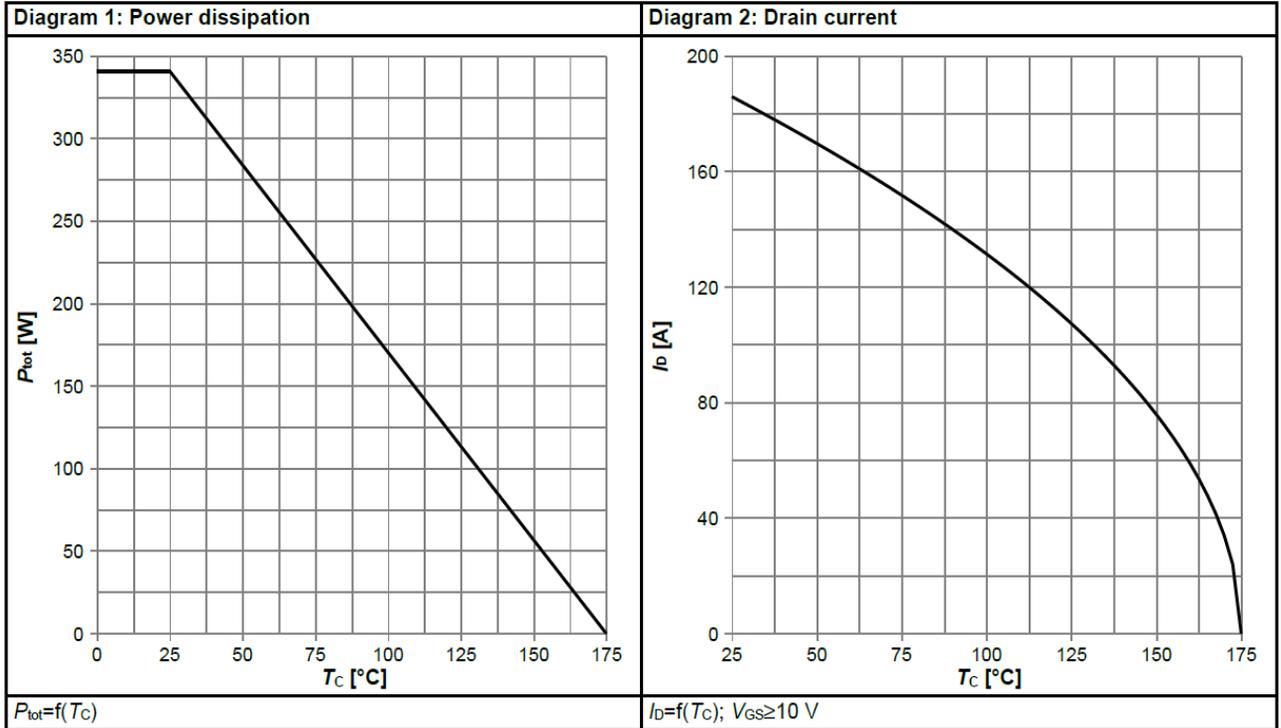
Symbol	Parameter	GSW150N15T H	GSJ150N15 TH	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.44	0.44	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.24	0.24	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	40	40	°C/W

Symbol	Parameter	Value (TO247)	Unit
$R_{\theta JA}^{(6)}$	Maximum Junction-to-Ambient	82	°C/W
$R_{\theta CS}^{(6)}$	Maximum Case-to-sink	0.6	°C/W
$R_{\theta JC}^{(7),(8)}$	Maximum Junction-to-Case $\theta$	4.1	°C/W

1. The power dissipation PD is based on  $T_J(\text{MAX})=150^\circ \text{C}$  in a TO251 package, using junction-to-case thermal resistance.
2. Repetitive rating, pulse width limited by junction temperature  $T_J(\text{MAX})=150^\circ \text{C}$ .
3.  $L=1\text{mH}$ , Starting  $T_J=25^\circ \text{C}$ .
4.  $L=10\text{mH}$ , starting  $T_J=25^\circ \text{C}$ .
5.  $L=60\text{mH}$ , starting  $T_J=25^\circ \text{C}$ .
6. The tests are performed with the device with  $T_A=25^\circ \text{C}$ .
7. The  $R_{\theta JA}$  is the sum of the thermal impedance from junction to case  $R_{\theta JC}$  and case to ambient.
8. These curves are based on the junction-to-case thermal impedance, assuming a maximum junction temperature of  $T_J(\text{MAX})=150^\circ \text{C}$ .

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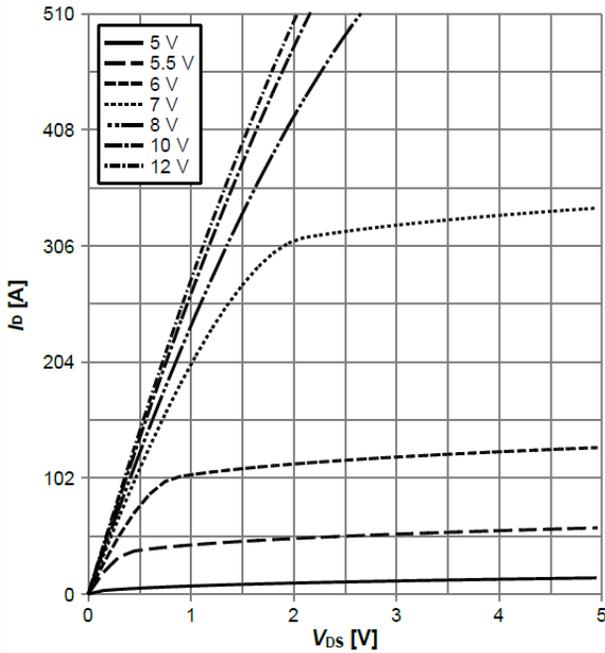
## Typical Performance Characteristics



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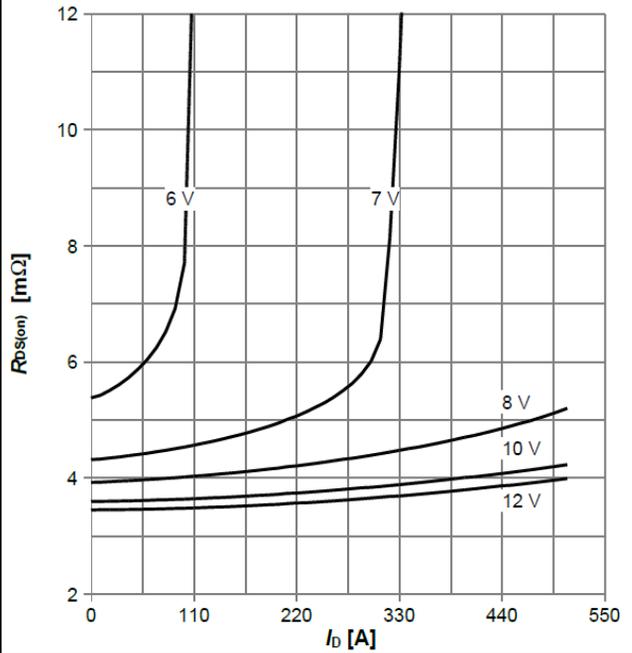
## Typical Performance Characteristics

Diagram 5: Typ. output characteristics



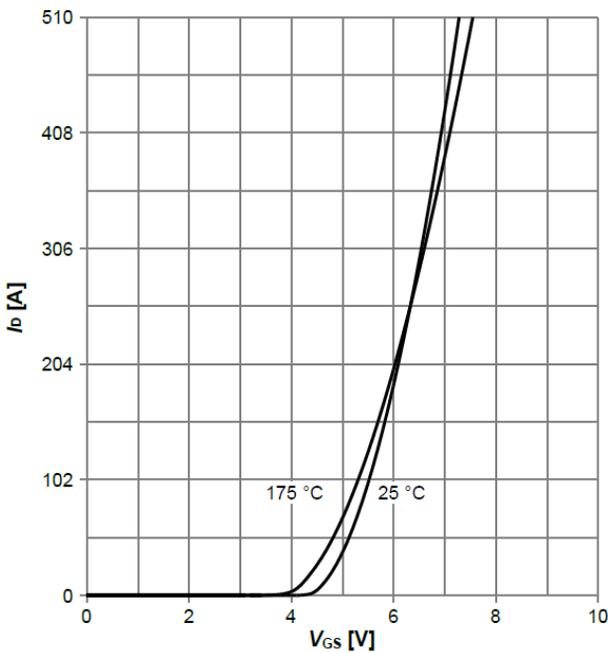
$I_D = f(V_{DS})$ ,  $T_J = 25^\circ\text{C}$ ; parameter:  $V_{GS}$

Diagram 6: Typ. drain-source on resistance



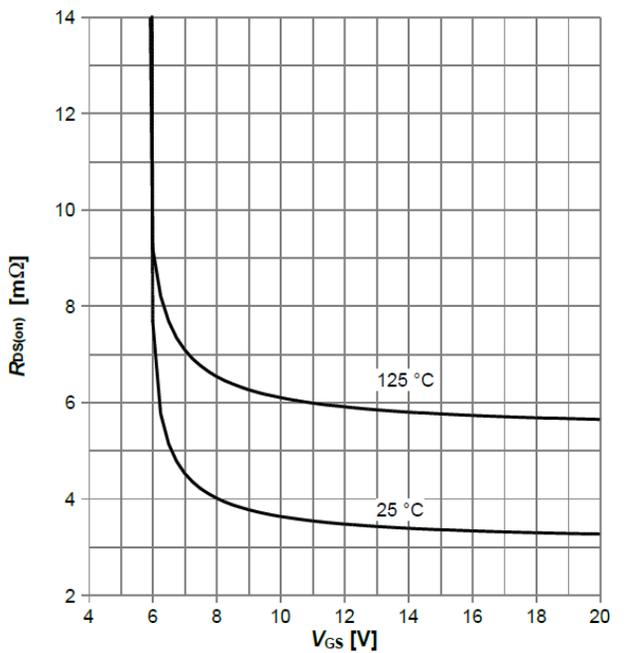
$R_{DS(on)} = f(I_D)$ ,  $T_J = 25^\circ\text{C}$ ; parameter:  $V_{GS}$

Diagram 7: Typ. transfer characteristics



$I_D = f(V_{GS})$ ,  $|V_{DS}| > 2|I_D|R_{DS(on)max}$ ; parameter:  $T_J$

Diagram 8: Typ. drain-source on resistance

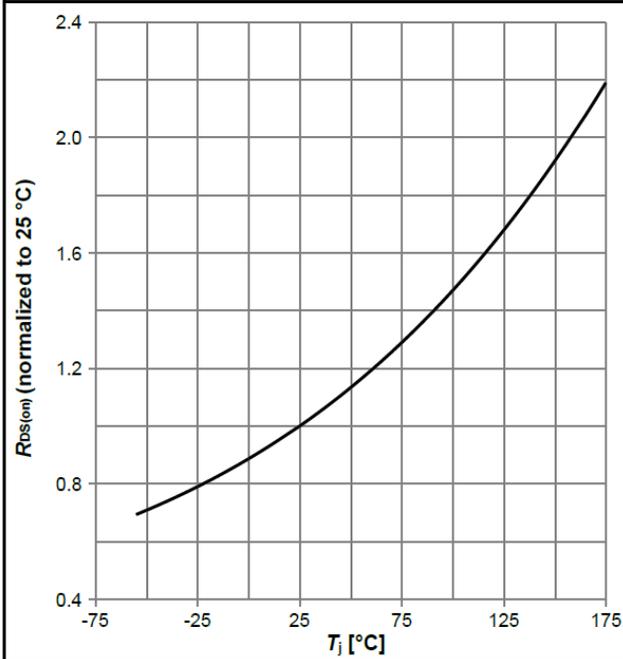


$R_{DS(on)} = f(V_{GS})$ ,  $I_D = 100\text{ A}$ ; parameter:  $T_J$

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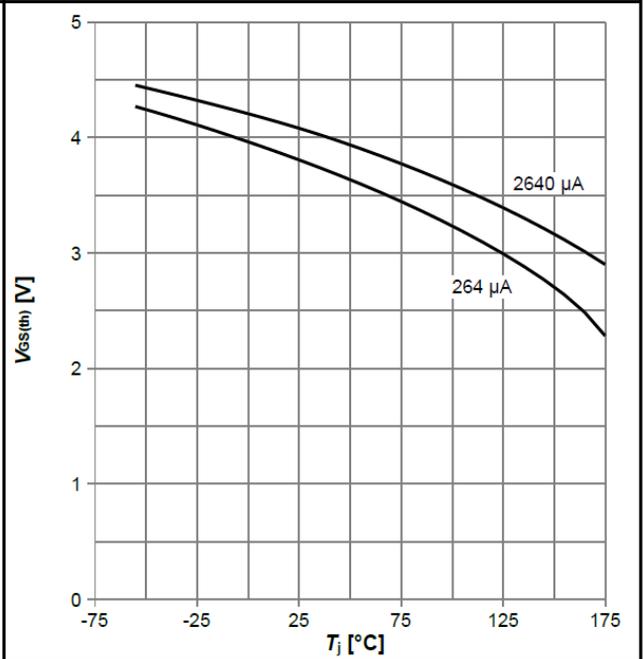
## Typical Performance Characteristics

Diagram 9: Normalized drain-source on resistance



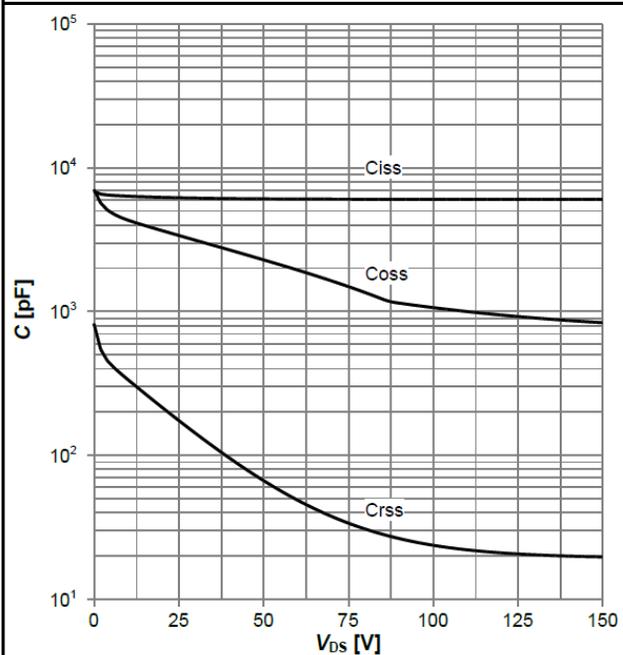
$R_{DS(on)}=f(T_j)$ ,  $I_D=100$  A,  $V_{GS}=10$  V

Diagram 10: Typ. gate threshold voltage



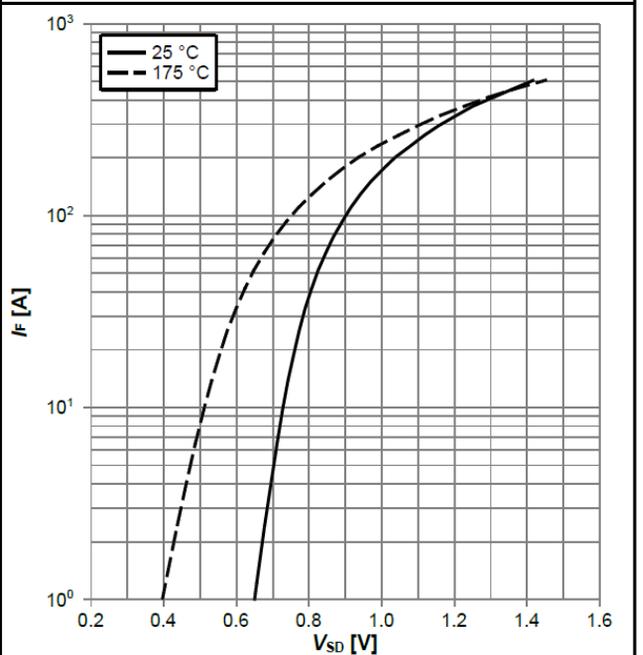
$V_{GS(th)}=f(T_j)$ ,  $V_{GS}=V_{DS}$ ; parameter:  $I_D$

Diagram 11: Typ. capacitances



$C=f(V_{DS})$ ;  $V_{GS}=0$  V;  $f=1$  MHz

Diagram 12: Forward characteristics of reverse diode

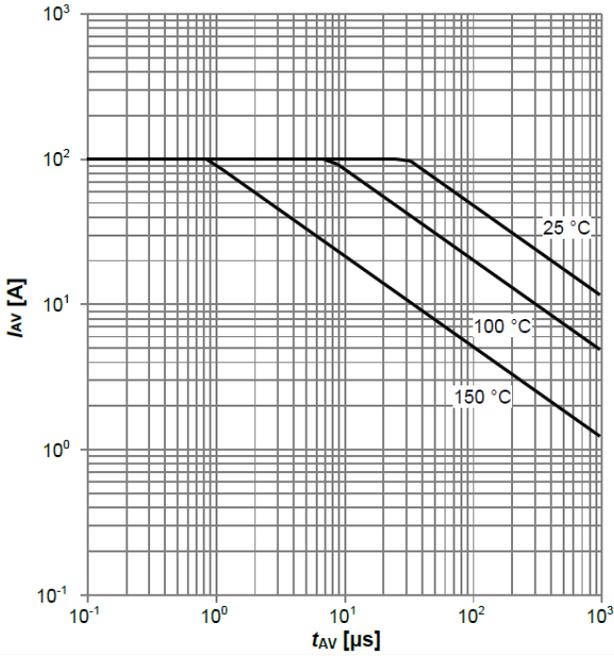


$I_F=f(V_{SD})$ ; parameter:  $T_j$

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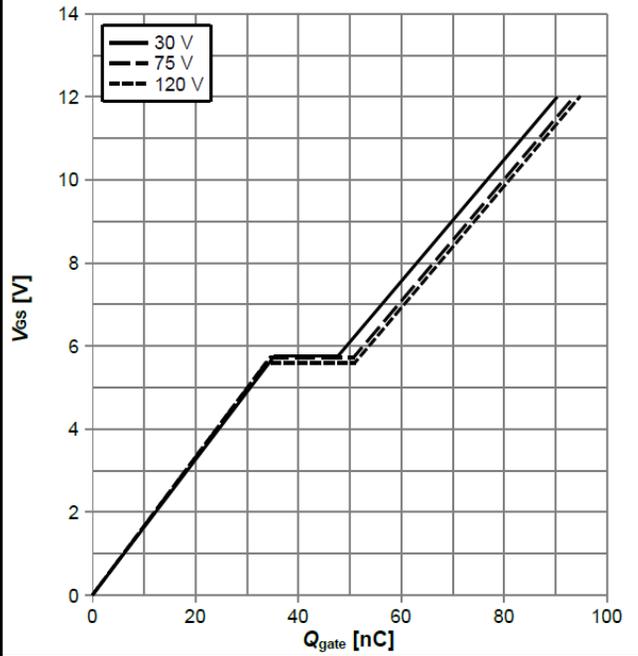
## Typical Performance Characteristics

Diagram 13: Avalanche characteristics



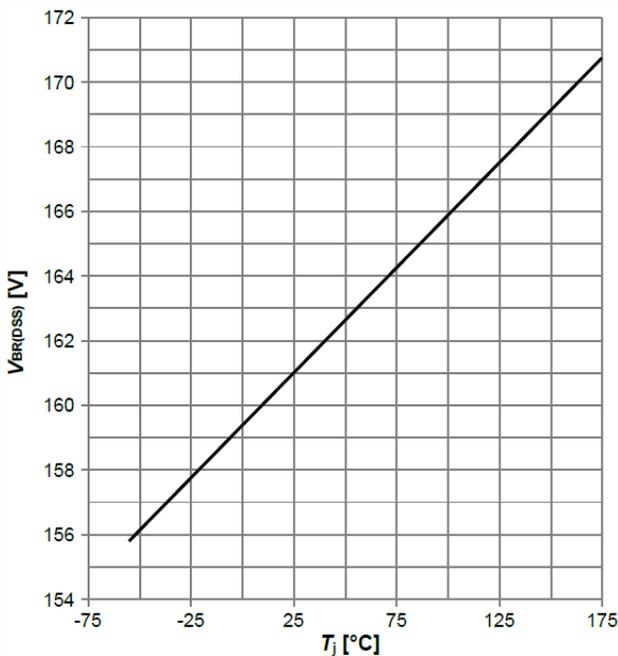
$I_{AS}=f(t_{AV}); R_{GS}=25 \Omega$ ; parameter:  $T_{j,start}$

Diagram 14: Typ. gate charge



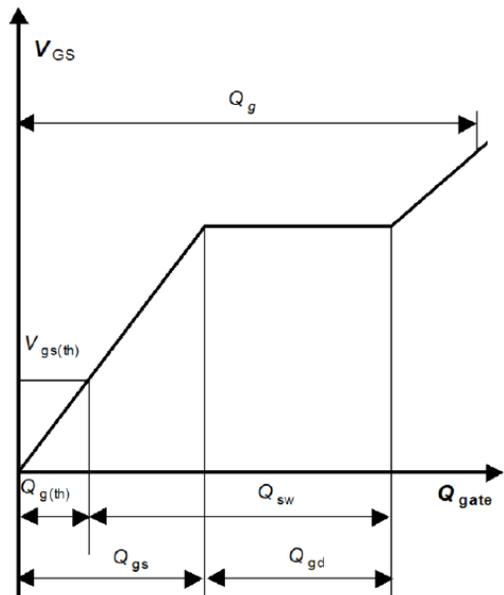
$V_{GS}=f(Q_{gate}), I_D=100 \text{ A pulsed}, T_j=25 \text{ }^\circ\text{C}$ ; parameter:  $V_{DD}$

Diagram 15: Drain-source breakdown voltage



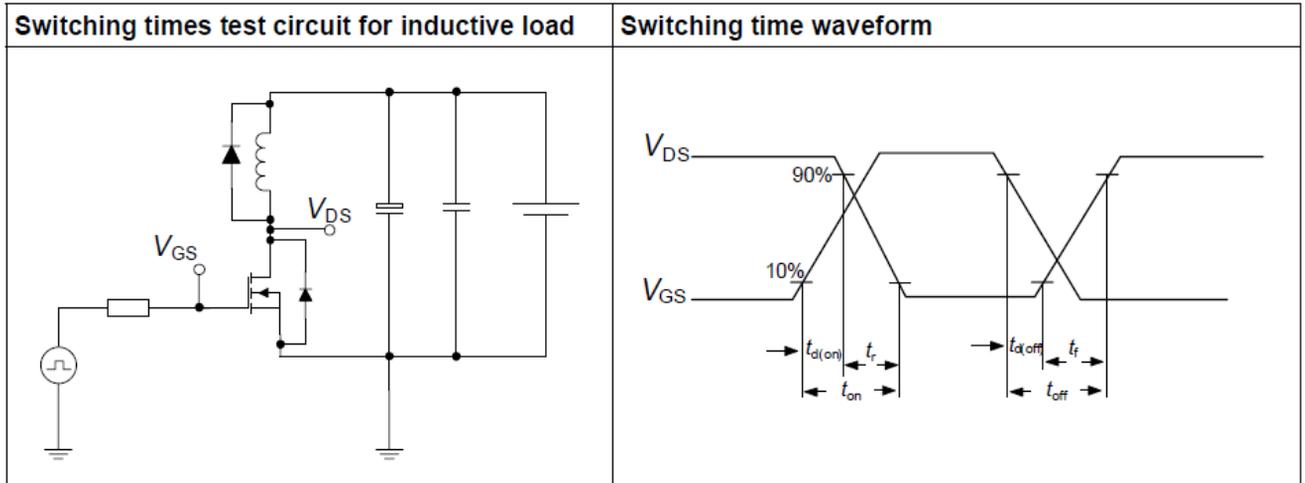
$V_{BR(DSS)}=f(T_j); I_D=1 \text{ mA}$

Diagram Gate charge waveforms

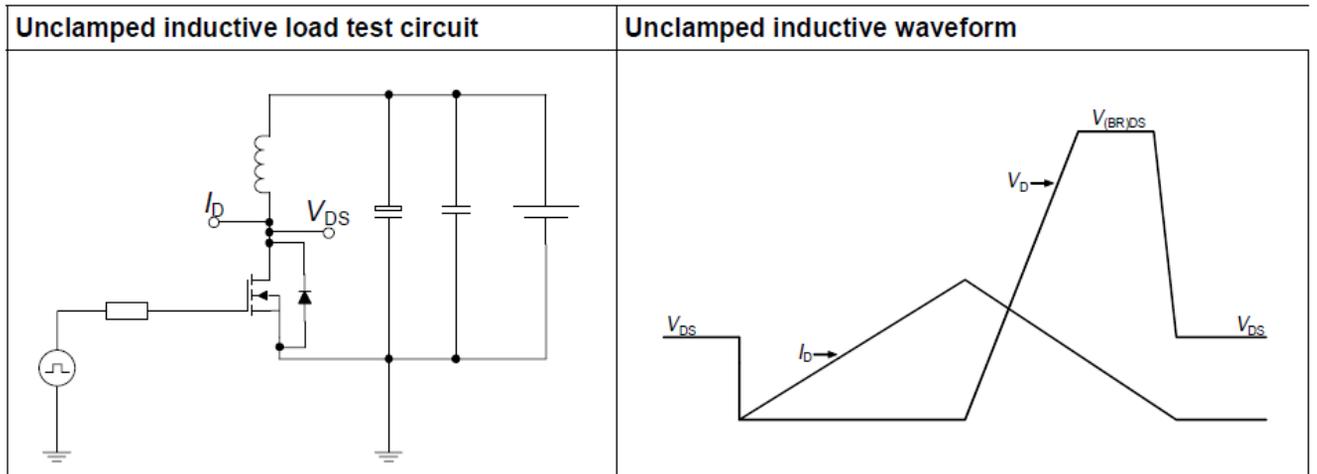


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**Table 20** Switching times test circuit and waveform for inductive load



**Table 21** Unclamped inductive load test circuit and waveform



**Table 22** Test circuit and waveform for diode characteristics

