



# GSX13N70E

## MOSFET

Metal Oxide Semiconductor Field Effect Transistor

Super Junction MOSFET

700V Super Junction Power Transistor

GSX13N70E

Data Sheet

Ver 1.1

2019-3-20

# 700V 13A Power MOSFET

## ■ Description

Group Semiconductor (GS) has series Multi-EPI Super-Junction power MOSFET platforms for voltage up 500V to 1000 volts, both with design service and manufacturing capability, including cell, termination design and simulation.

The GS 700V 13A Power MOSFET is a Low voltage N channel Multi-EPI Super-Junction power MOSFET sample with advanced technology to have better characteristics, such as fast switching time, low C<sub>iss</sub> and C<sub>rss</sub>, low on resistance and excellent avalanche characteristics, making it especially suitable for applications which require superior power density and outstanding efficiency.



## ■ Features

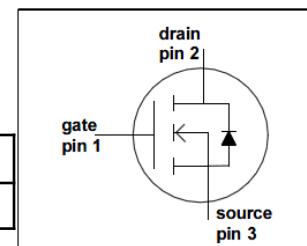
RDS(ON)=0.38Ω @VGS = 10V

VDS = 700V

ID (@ VGS=10V) = 6.5A

## ■ PKG

GSA13N70E	GSP13N70E	GSB13N70E	GSD13N70E	GSS13N70E
TO-220FullPak	TO-220	TO-263	TO-252	TO-251



## ■ Absolute Maximum Ratings (TC = 25° C, unless otherwise specified)

Symbol	Parameter	GSP13N70E	GSA13N70E	Unit
V <sub>DSS</sub>	Drain-Source Voltage	700		V
I <sub>D</sub>	Drain Current - Continuous (TC = 25°C) - Continuous (TC = 100°C)	13* 9*		A
I <sub>DM</sub>	Drain Current - Pulsed (Note 1)	46		A
V <sub>GSS</sub>	Gate-Source voltage	±30		V
E <sub>A</sub>	Single Pulsed Avalanche Energy (Note 2)	280		mJ
I <sub>AR</sub>	Avalanche Current (Note 1)	2.3		A
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)	1.2		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15		V/ns
dVds/dt	Drain Source voltage slope (V <sub>ds</sub> =480V)	50		V/ns
P <sub>D</sub>	Power Dissipation (TC = 25°C)	151	35	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150		°C
T <sub>L</sub>	Max. Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		°C

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## ■ Thermal Characteristics

Symbol	Parameter	GSA13N70E	GSP13N70E	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	1.2	1.2	°C/W
R <sub>θCS</sub>	Thermal Resistance, Case-to-Sink Typ.	0.5	0.5	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	62	62	°C/W

Symbol	Parameter	Value (TO220)	Unit
R <sub>θJA</sub> <sup>(6)</sup>	Maximum Junction-to-Ambient	82	°C/W
R <sub>θCS</sub> <sup>(6)</sup>	Maximum Case-to-sink	0.6	°C/W
R <sub>θJC</sub> <sup>(7),(8)</sup>	Maximum Junction-to-Case θ	4.1	°C/W

1. The power dissipation PD is based on TJ(MAX)=150° C in a TO251 package, using junction-to-case thermal resistance.
2. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150° C.
3. L=1mH, Starting TJ=25° C.
4. L = 10mH, starting TJ = 25° C.
5. L=60mH, starting TJ = 25° C.
6. The tests are performed with the device with T A =25° C.
7. The R □ JA is the sum of the thermal impedance from junction to case R □ JC and case to ambient.
8. These curves are based on the junction-to-case thermal impedance, assuming a maximum junction temperature of TJ(MAX)=150° C.



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## ■ Electrical Characteristics (TJ=25° C unless otherwise specified)

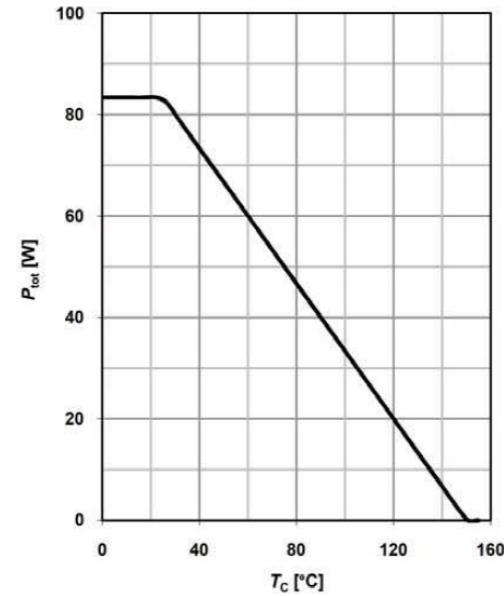
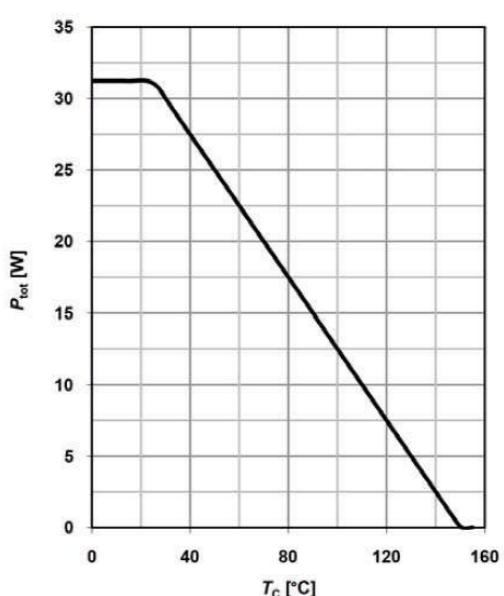
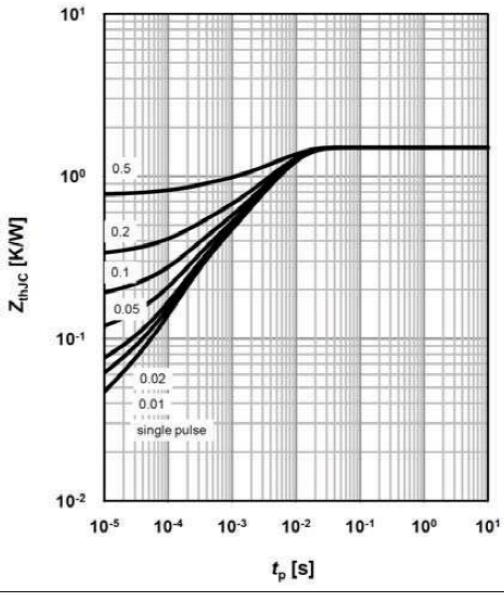
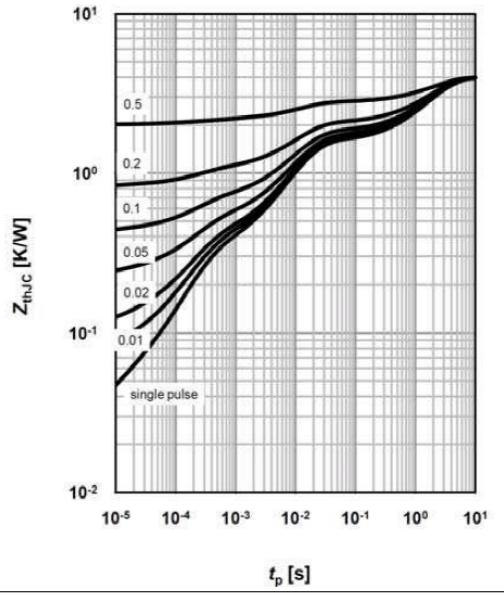
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA, T <sub>J</sub> = 25°C	700	--	--	V
		V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA, T <sub>J</sub> = 150°C	--	750	--	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250µA, Referenced to 25°C	--	0.6	--	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 700V, V <sub>GS</sub> = 0V -T <sub>J</sub> = 150°C	-- 10	-- -	1	µA µA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30V, V <sub>DS</sub> = 0V	--	--	100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30V, V <sub>DS</sub> = 0V	--	--	-100	nA
<b>On Characteristics</b>						
V <sub>G(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA	2.5	--	4.5	V
R <sub>D(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6.5A	--	0.36	0.38	Ω
g <sub>F</sub> S	Forward Transconductance	V <sub>DS</sub> = 40V, I <sub>D</sub> = 6.5A	--	16	--	S
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	--	780	-	pF
C <sub>oss</sub>	Output Capacitance		--	23	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		--	1.9	--	pF
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 400V, I <sub>D</sub> = 6.5A R <sub>G</sub> = 20Ω (Note 4)	--	17	--	ns
t <sub>r</sub>	Turn-On Rise Time		--	12	--	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		--	130	--	ns
t <sub>f</sub>	Turn-Off Fall Time		--	10	--	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 400V, I <sub>D</sub> = 6.5A V <sub>GS</sub> = 10V (Note 4)	--	34	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	4.5	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	19	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current	--	--	10	--	A
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current	--	--	35	--	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 6.5A	--	0.9	1.5	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, I <sub>S</sub> = 6.5A dI/dt = 100A/µs	--	300	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	3.5	--	µC

### NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L=60mH, I<sub>AS</sub>=3A, V<sub>DD</sub>=150V, Starting T<sub>J</sub>=25 °C
3. I<sub>SD</sub><4.5A, dI/dt ≤ 200A/us, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25 °C
4. Pulse Test: Pulse width ≤ 300us, Duty Cycle ≤ 2%
5. Essentially Independent of Operating Temperature Typical Characteristics

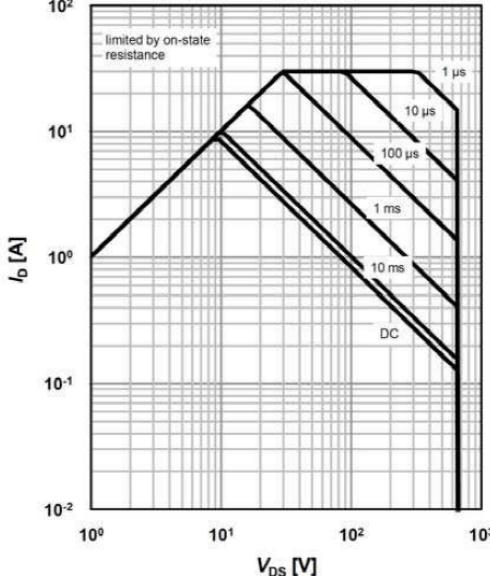
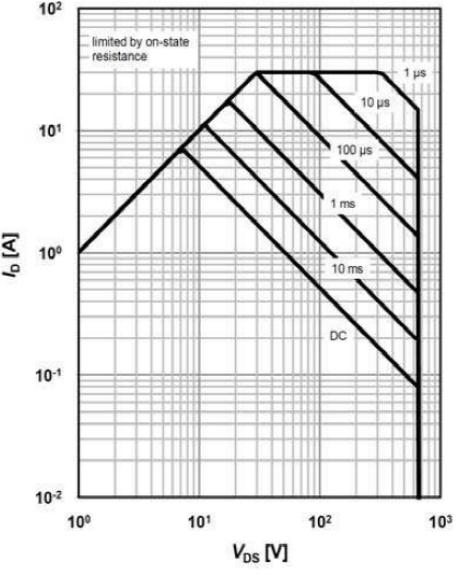
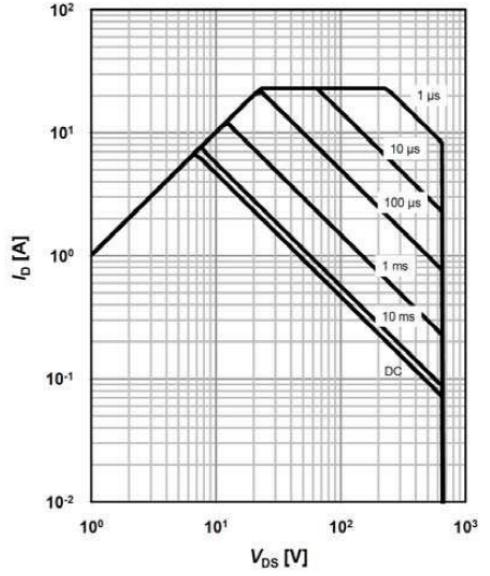
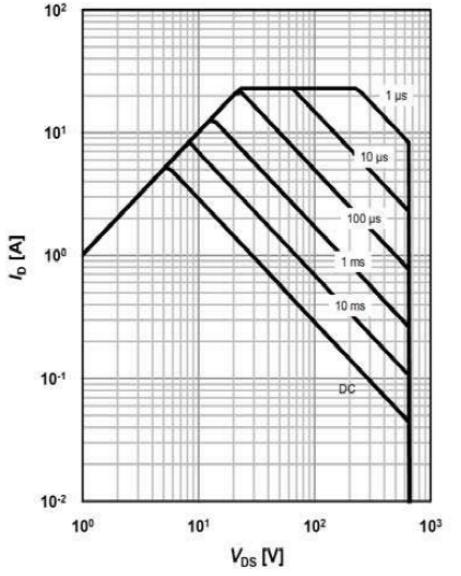
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## Typical Performance Characteristics

Power dissipation Non FullPAK	Power dissipation FullPAK
 <p><math>P_{\text{tot}} = f(T_c)</math></p>	 <p><math>P_{\text{tot}} = f(T_c)</math></p>
Max. transient thermal impedance Non FullPAK	Max. transient thermal impedance FullPAK
 <p><math>Z_{(\text{thJC})} = f(t_p)</math>; parameter: <math>D = t_p/T</math></p>	 <p><math>Z_{(\text{thJC})} = f(t_p)</math>; parameter: <math>D = t_p/T</math></p>

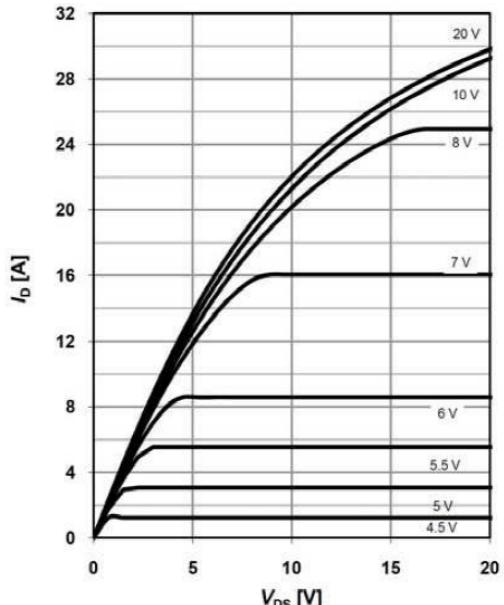
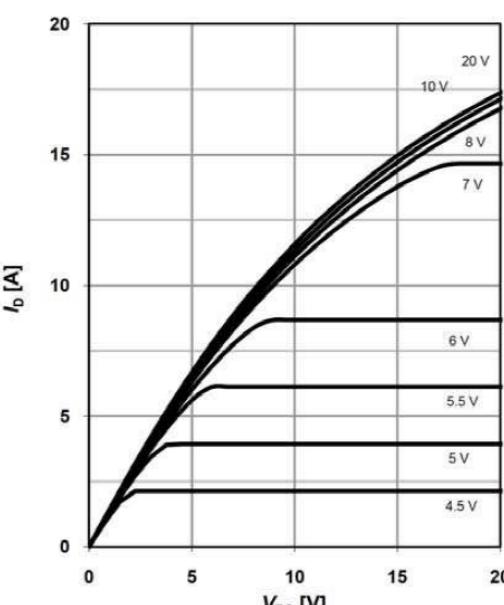
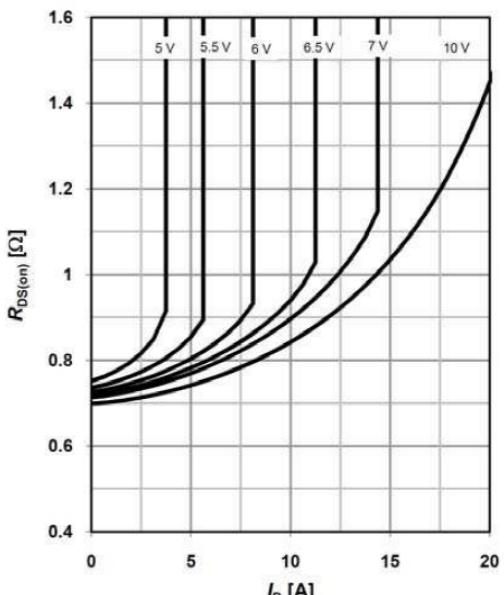
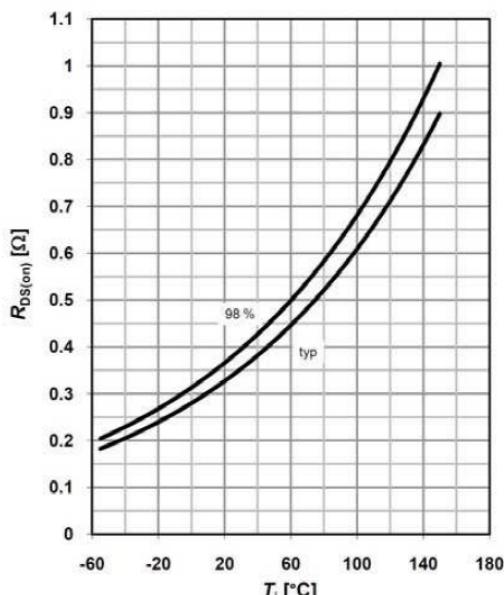
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## Typical Performance Characteristics

Safe operating area $T_c=25\text{ }^\circ\text{C}$ Non FullPAK	Safe operating area $T_c=25\text{ }^\circ\text{C}$ FullPAK
	
$I_D=f(V_{DS})$ ; $T_c=25\text{ }^\circ\text{C}$ ; $V_{GS} > 7\text{V}$ ; D=0; parameter $t_p$	$I_D=f(V_{DS})$ ; $T_c=25\text{ }^\circ\text{C}$ ; $V_{GS} > 7\text{V}$ ; D=0; parameter $t_p$
Safe operating area $T_c=80\text{ }^\circ\text{C}$ Non FullPAK	Safe operating area $T_c=80\text{ }^\circ\text{C}$ FullPAK
	
$I_D=f(V_{DS})$ ; $T_c=80\text{ }^\circ\text{C}$ ; $V_{GS} > 7\text{V}$ ; D=0; parameter $t_p$	$I_D=f(V_{DS})$ ; $T_c=80\text{ }^\circ\text{C}$ ; $V_{GS} > 7\text{V}$ ; D=0; parameter $t_p$

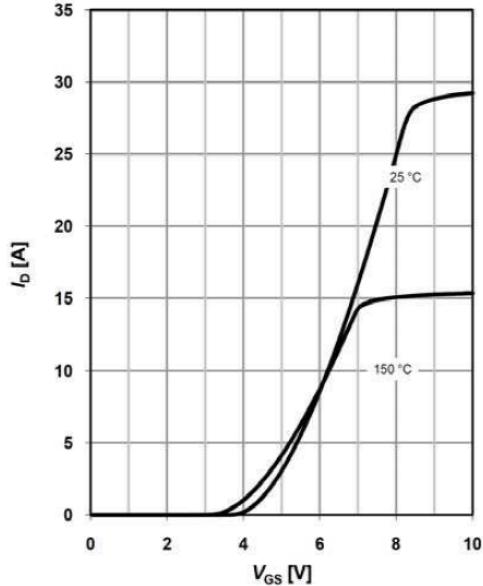
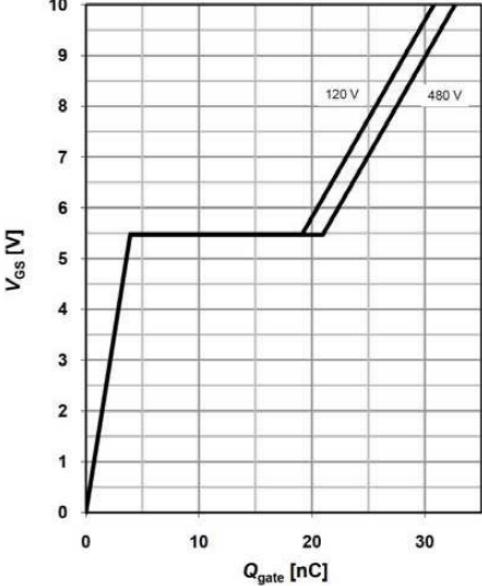
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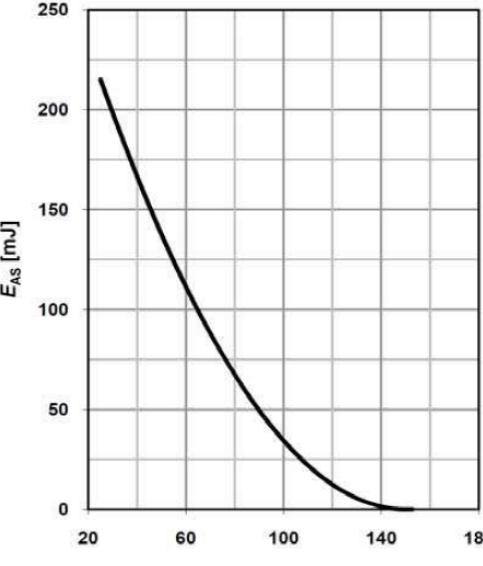
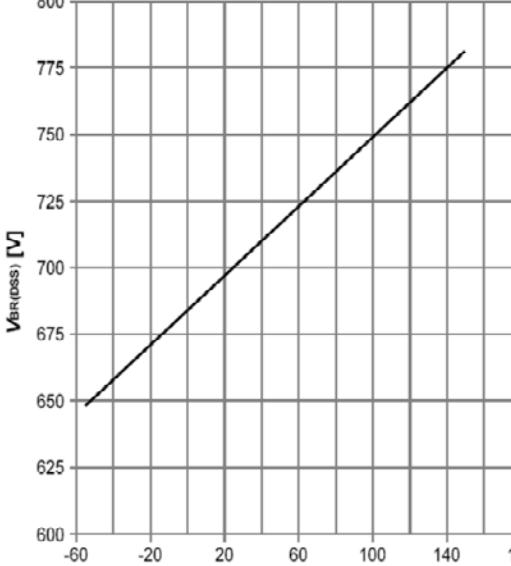
## Typical Performance Characteristics

Typ. output characteristics $T_c=25\text{ }^\circ\text{C}$	Typ. output characteristics $T_j=125\text{ }^\circ\text{C}$
	
$I_D=f(V_{DS})$ ; $T_c=25\text{ }^\circ\text{C}$ ; parameter: $V_{GS}$	$I_D=f(V_{DS})$ ; $T_j=125\text{ }^\circ\text{C}$ ; parameter: $V_{GS}$
Typ. drain-source on-state resistance	Drain-source on-state resistance
	
$R_{DS(on)}=f(I_D)$ ; $T_j=125\text{ }^\circ\text{C}$ ; parameter: $V_{GS}$	$R_{DS(on)}=f(T_j)$ ; $I_D=4.9\text{ A}$ ; $V_{GS}=10\text{ V}$

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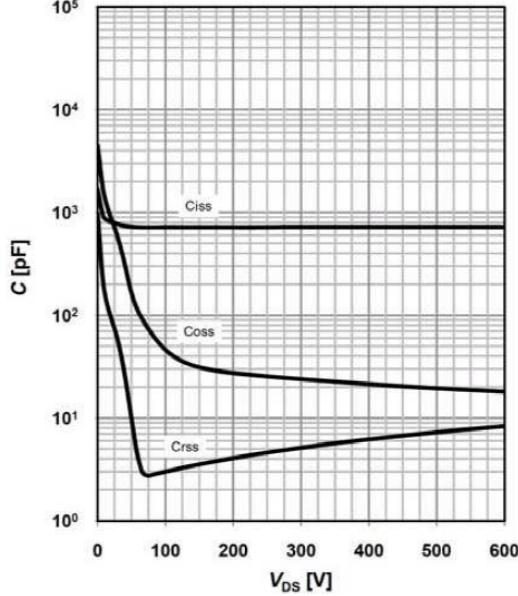
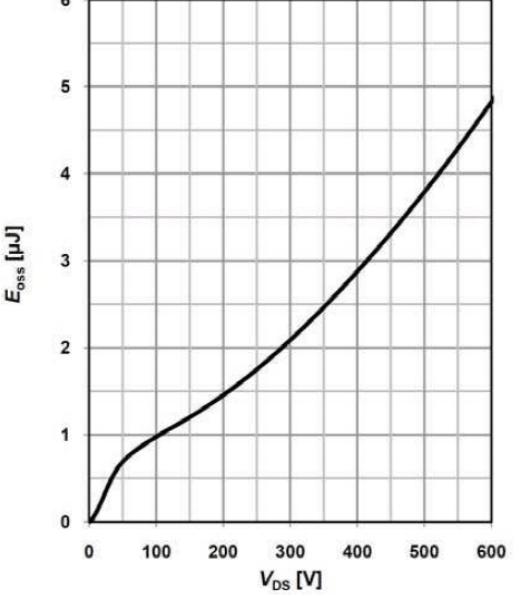
## Typical Performance Characteristics

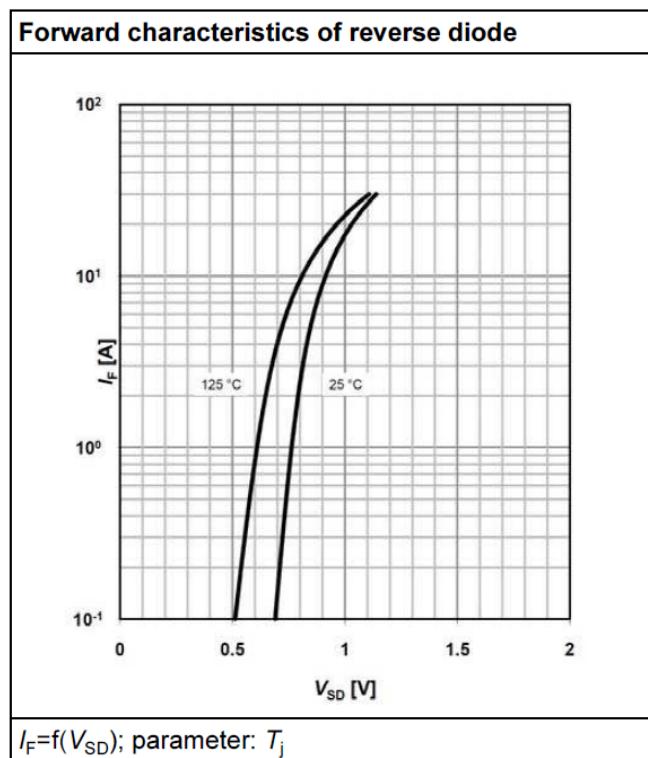
Typ. transfer characteristics	Typ. gate charge
 <p><math>I_D = f(V_{GS})</math>; <math>V_{DS} = 20V</math></p>	 <p><math>V_{GS} = f(Q_{gate})</math>, <math>I_D = 4.9 A</math> pulsed</p>

Avalanche energy	Drain-source breakdown voltage
 <p><math>E_{AS} = f(T_j)</math>; <math>I_D = 1.8 A</math>; <math>V_{DD} = 50 V</math></p>	 <p><math>V_{BR(DSS)} = f(T_j)</math>; <math>I_D = 1.0 mA</math></p>

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## Typical Performance Characteristics

Typ. capacitances	Typ. $C_{oss}$ stored energy
 <p><math>C = f(V_{DS})</math>; <math>V_{GS} = 0</math> V; <math>f = 1</math> MHz</p>	 <p><math>E_{oss} = f(V_{DS})</math></p>



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Table 20 Switching times test circuit and waveform for inductive load

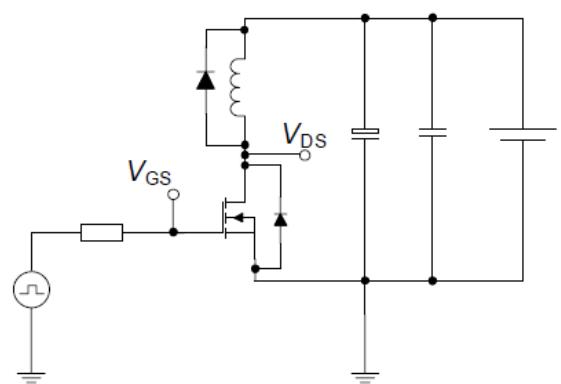
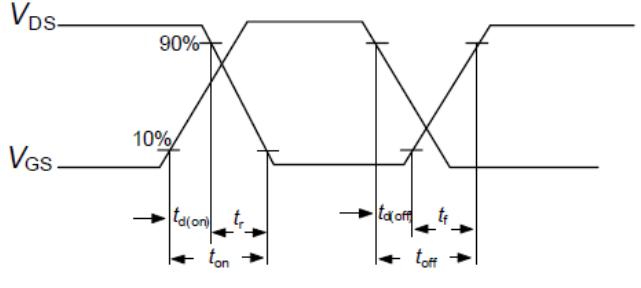
Switching times test circuit for inductive load	Switching time waveform
	

Table 21 Unclamped inductive load test circuit and waveform

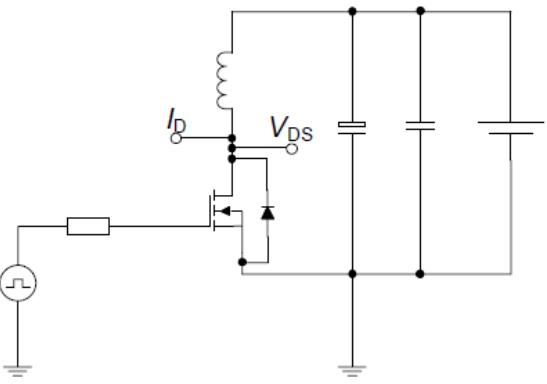
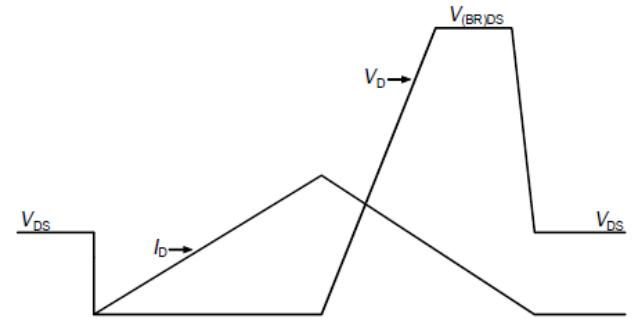
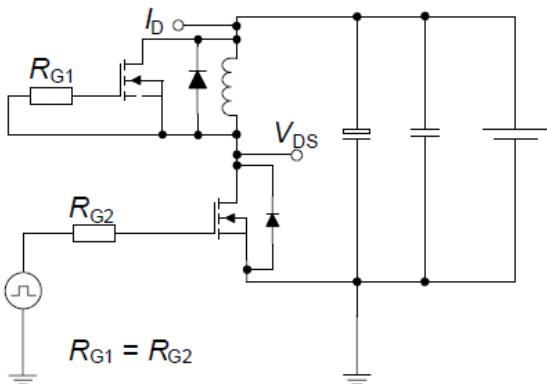
Unclamped inductive load test circuit	Unclamped inductive waveform
	

Table 22 Test circuit and waveform for diode characteristics

Test circuit for diode characteristics	Diode recovery waveform
 <p><math>R_{G1} = R_{G2}</math></p>	